#### VISUALIZE THE FUTURE



#### Fiscal Year Ended March 31, 2018

# **Results Briefing**

Digital Media Professionals Inc.

May 21, 2018

The views and forecasts that appear in these materials represent determinations made by the Company at the time the materials were created. The accuracy of the information therein is not guaranteed. Please be aware of the possibility that actual performance and results may differ considerably due to a variety of factors.



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## **1** Explanation of Results, Fiscal Year Ended March 31, 2018

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Fiscal Year Ending March 31, 2019, Business Forecast

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Measures in the Time Ahead and Proposition for Future Growth

## **Operating Environment of the Company**



#### **Semiconductor Industry**

- The semiconductor market in 2017 surged 21.7% from the year earlier to US\$412.2 billion, surpassing US\$400 billion for the first time ever owing to expanded application in servers and data mining despite facing headwinds from a slump in iPhoneX sales and falling flash memory prices (WSTS)
- Decreased demand resulting from reduced Chinese smartphone production, acceleration of Chinese semiconductor development and other factors arising from trade regulations aimed at Chinese companies due to worsening relations between the US and China could bring about seismic changes in industrial power relationships
- The model wherein set vendors and IT vendors engage directly in the development of semiconductors is accelerating as semiconductors come with increasingly high added value

#### **AI / Visual Computing Field**

- AI chip market forecast: 6-year CAGR from 2017 of 50%, with AI chip sales surpassing US\$11 billion in 2023 (Allied Market Research 2018)
- Use of AI in graphics processing is accelerating. In particular, more than 60% of security camera and automotive products use AI (Embedded Vision Alliance Survey Dec. 2017)



# Successful return to black figures due to significantly higher sales in all operations

- LSI operations (sales increased ¥149 million compared with the previous fiscal year)
  - Significant contribution from initiation of volume production of next-generation graphics processing LSI "RS1"

#### • IP core licensing operations

(sales increased ¥72 million compared with the previous fiscal year)

- Increased running royalties from existing customers in game machines and digital cameras, etc.
- Licensing of the new GPU IP core "K3000" to new and existing customers

#### Professional services operations

(sales increased ¥57 million compared with the previous fiscal year)

- Increased sales of AI-related software and commissioned projects from automotive equipment manufacturers
- Additional new order receipts relating to commissions for the acceleration of NEDO "Development commissions for power-saving AI engines"



(Unit: million yen)

	FY 03/2017	FY 03/2018	YoY change	
	1103/2017		(Amount)	(Increase- decrease rate)
Net sales	694	973	279	40.3%
Operating income	-263	69	333	
Ordinary income	-262	66	328	
Net income	-365	109	474	

✓ Return to black figures for operating income due to increase in net sales and decrease in SG&A

✓ Impact on net income from recognition of extraordinary gain from cancellation of distributor agreement



(Unit: million yen)

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		End of March 2017	End of March 2018	Increase-decrease amount
	Current assets	1,668	1,774	106
	Non-current assets	112	346	233
То	tal assets	1,780	2,121	340
	Current liabilities	91	228	137
	Non-current liabilities	18	18	0
Tot	al liabilities	110	247	137
Tot	al net assets	1,670	1,873	203
	tal liabilities and net sets	1,780	2,121	340

✓ Equity ratio of 88% remains at a high level



#### Initiation of Volume Production and Shipments of RS1 Next-generation Graphics Processor

\*Decisions made for adoption at several major leading customers

\*Initiation of volume production and shipments in fiscal year ended March 31, 2018

#### Industry Trends and Forecast for RS1



High added-value module with SoC and DDR memory in same package

- February 2018 revision of "Entertainment and Amusement Trade and the Implementation Rules for the Entertainment and Amusement Trades Rationalizing Act" took effect
- Fiscal year ending March 31, 2019 is off-season for transition to new standard machines, and market environment is expected to be harsh for industry as a whole
- Switch to new standard machines will progress in fiscal year ending March 31, 2020, and RS1 shipments will be in stride, so it is expected to be leading graphics chip in industry by mid-2020

### Fiscal Year Ended March 31, 2018 **Topics**



Z : A <sup>™</sup> DV 700

#### Licensing start of "ZIA DV700" AI processor IP

 Processor with ultra-low power consumption specialized for Deep Learning inference process



"ZIA Classifier" adopted for the drive recorder AI service provided by Sumitomo Mitsui Auto Service Company, Limited, the top auto leasing company in Japan

## In the auto leasing industry the first application of AI technology to video analysis

Performs automatic drive-recorder video analysis to identify the specific driving behavior (causative behavior) that leads to dangerous driving, combined with an external camera video (behavior result) analysis and report





behavior

- Talking on and operating mobile phones/smartphones
- Looking away or driving with one hand



- Operating computers or other such devices
- Operating GPS/audio systems

### Fiscal Year Ended March 31, 2018 **Topics**



Collaboration with Altima and Morpho on Al/Deep Learning technology using Intel® FPGA (November 2017)



#### ZIA DV series AI processor

- Advanced processor technology
- Hardware optimization technology



- Model designing
- Model optimization technology

**b** Morpho

Provision of programmable solutions using Intel FPGA

**MACNÍCA** 



## **Provision of FPGA platform with DV700**

#### Accelerate introduction of edge AI

Development/evaluation made possible based on Morpho's object recognition DNN\* learned model

Fast application to development/evaluation of inference environments and embedded devices in volume production

\* Deep Neural Networks

# **NEDO** (Crosscutting technology development project to promote IoT)



# Development of AI platform based on a power saving AI engine and a cloud integrating dissimilar engines

Amount awarded DMP under the contract (total amount)

¥475 million (existing portion)¥108 million(additional portion in the current FY)

Duration of the commission

June 2016 - March 2019

#### **Project outline**

- Development of a power saving AI engine which enables AI algorithms to process 10 times more efficient than predecessors did
- Development of an AI platform for integrating dissimilar engines on the cloud side



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## **Accelerate DMP AI Business**



## **1** Explanation of Results, Fiscal Year Ended March 31, 2018

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# Fiscal Year Ending March 31, 2019, Business Forecast

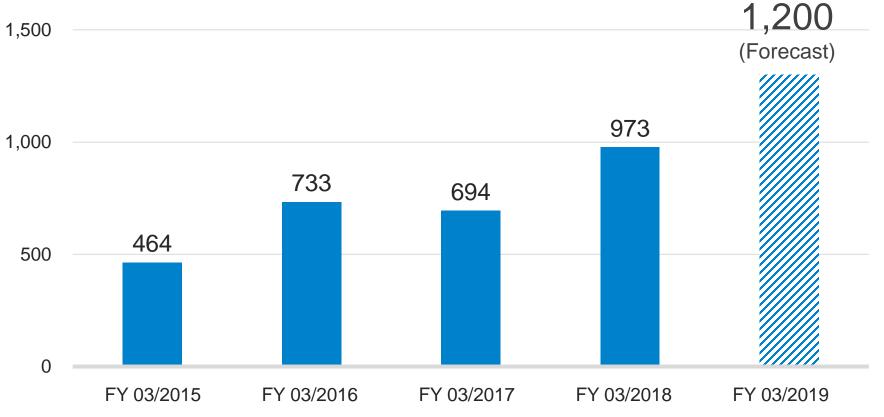
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Measures in the Time Ahead and Proposition for Future Growth

### Fiscal Year Ending March 31, 2019 Business Forecast

Expectations for 23% year-on-year growth in sales revenue due to focus on expanding sales volume of next-generation graphics processing semiconductor "RS1," for which volume production and shipping began in the previous fiscal year

#### Net sales (Unit: million yen)





Net sales, operating income and ordinary income all to increase due to increased RS1 sales volume and expansion of AI products and services

(Unit: million yen)

	FY 03/2018	FY 03/2019	YoY change	
	(Actual)	(Forecast)	(Amount)	(Increase- decrease rate)
Net sales	973	1,200	227	23.2%
Operating income	69	100	31	43.3%
Ordinary income	66	100	34	50.6%
Net income	109	90	-19	-17.6%







Fiscal Year Ending March 31, 2019, Business Forecast



# Measures in the Time Ahead and Proposition for Future Growth

### Measures in the time ahead

#### Full-scale launch of SoC business through launch of RS1 volume production

• Aim to develop industry standard platform by having it adopted by several major leading customers and focus on support for customer product launches

#### Expansion of ZIA platform product sales and portfolio

- Cultivation of new ZIA Classifier customers
- Promotion of DV700/DV500 IP licensing and FPGA model business

#### Promotion of alliances

- Provision of AI solutions through collaboration with partner companies
- Promotion of development commissions from NEDO for power-saving Al engines

# Promotion of Al-related professional services for automotive equipment manufacturers

#### **Promotion of strategic partnerships**

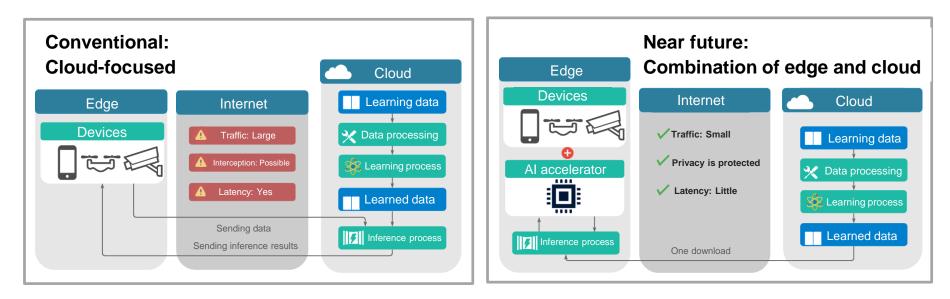
In regard to our work with business and capital alliance partner UKC Holdings, we will collaborate as a channel partner in LSI operations and, in the future, jointly develop solutions for AI.

株式会社 UKCホールディングス



# As learning processes handling big data are processed on the cloud, it will become mainstream for inference processes to utilize <u>edge AI</u>

- ✓ Superior in terms of real-time properties
- ✓ Protection of privacy
- ✓ Reduction of network bandwidth and power



75% of inference processes handled on edge or combination of edge and cloud (Embedded Vision Alliance Survey Dec. 2017)

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#### Use of FPGA (rewritable logic circuit LSI) as AI accelerator is increasing

- ✓ Higher power performance than general-purpose GPU
- ✓ Superior to ASIC in terms of rewritability and flexibility, making low cost development possible
- ✓ Follows latest AI algorithms, making quick commercialization possible
- ✓ Supports long-term parts supply

	CPU	GPU	FPGA	ASIC
Real time properties	×	$\bigcirc$	0	O
Low power consumption	×	×	0	Ø
Flexibility	$\bigcirc$	0	0	×
Long-term supply	$\bigtriangleup$	×	Ø	0



40% of customers use FPGA as graphics processor/AI semiconductor (Embedded Vision Alliance Survey Mar. 2018)

### **Expansion of ZIA Platform**



#### Deep Learning / AI / ZIA™

**Z ¦ A**<sup>™</sup> Z Intelligence Accelerator

Based on the technologies accumulated since the founding as Japan's only GPU vendor, the AI platforms of DMP form novel software/hardware products fusing AI/Deep Learning knowledge.



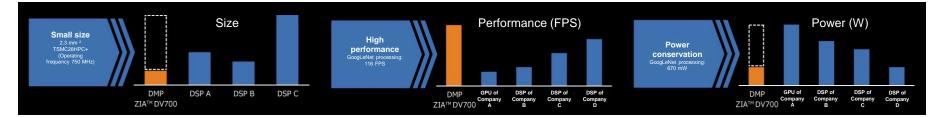
### Processor IP core with ultra-low power consumption specialized for Deep Learning inference processing applicable to all data categories

# **Z¦A**<sup>™</sup> DV700

- The second product of the "ZIA" product platform using AI technology of DMP
- Processor IP with ultra-low power consumption suited for edge-computing AI processing, capable of inference processing applicable to all data categories including images, videos, and voice data, etc.
- Combines low power consumption and high performance with flexible response capability regarding functions and performances required for a diversity of applications including security cameras



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### Edge AI processor supporting low-cost FPGA Accelerate edge AI implementation in industrial and automotive



Optimized for scene understanding and object recognition (SSD/SegNET) often used in industrial equipment and automated driving systems



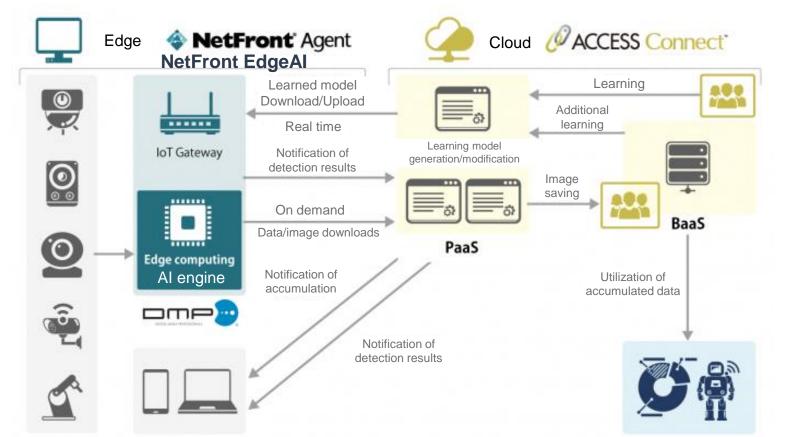
Can be used with low-end FPGA such as Intel Cyclone V and Xilinx Zynq7000 (Note: Photograph is of Cyclone® V SX SoC development board)

◆ Example of object detection by ZIA<sup>™</sup> DV500 (SSD Multibox detection) Detects car, motorcycle and human on bicycle

## Partnership with ACCESS (April 2018)



Collaboration with ACCESS, a one-stop provider of IoT solutions, on converting IoT and edge computing technology for use with AI (April 2018)

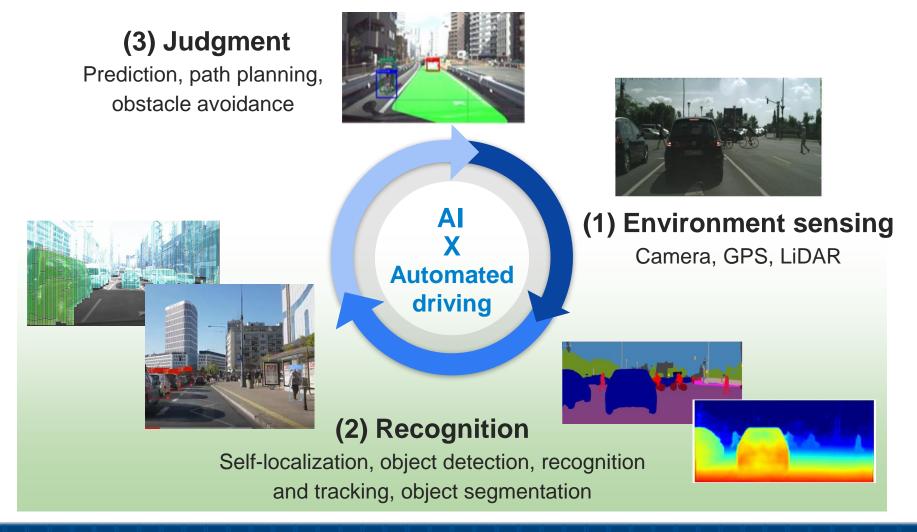


Edge-learned model updates, life/death monitoring and firmware updates can be performed remotely

## **DMP - Automated Driving Technology**

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Development of automated driving algorithms, CPU/GPU implementation and development of hardware accelerator



## New Establishment of Cyber Al Division (March 2018)

Engineers from various countries specializing in AI, GPU and computer science are working on R&D utilizing AI and development of innovative products and services at DMP.

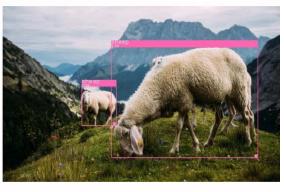




CyberAlLab 🖬

#### Stixel from Single Monocular Images

Stixel is a Medium-level representation of images on traffic scenes, indicating free space, the obstacles ahead and their approximate depth. Usually a stereo image pair is needed to compute the Disparity (Depth) Map. In this post, we in...



CyberAlLab 🖬

SSD Multibox Detection Running on FPGA (DV500/DV700)

This post demonstrates the Single Shot Multibox Detector(SSD) result on PC

The DMP Cyber AI Division website will introduce DMP's AI initiatives. Computer Vision **FPGA** Machine Learning

Self-driving Car

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### **Proposition for Future Growth**



#### **Establishment of new growth domains**

#### **Expansion into the growing IoT/AI field**

Integrated development of AI algorithms, software and hardware

#### Establishment of a SoC business platform in the amusement market where we can win

- Volume production of new product "RS1"
- Aim to make it an industry standard platform

#### **Development of ZIA platform**

- Al processors for edge computing
- Software
- Modules
- Provision of solutions with partners
- Overseas development

#### Expansion of professional services

#### centered on automotive industry

Automated driving algorithms

#### Expansion of the SoC / Module Business

- Top share in amusement SoC
- High-volume AI products

Current position

Phase 1

#### Phase 2

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Phase 3



## Thank you for listening.

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